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APPLICANTS

Hisashi Ohtani, Kanagawa, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/967,697 09/28/2001 PAT 6,657,228  
 which is a CON of 08/862,895 05/23/1997 PAT 6,316,787 *Yes m*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 8-165272 06/04/1996 *Yes m*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>[Signature]</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 8
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TITLE  
 Semiconductor integrated circuit and fabrication method thereof

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